

INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(use as many sheets as necessary)

Attorney Docket Number	34176/LTR/B600
Application Number	09/252,551 ^{09/620,818}
Filing Date	February 18, 1999
Applicant(s)	Henry Samueli, et al.
Group Art Unit	2732
Examiner Name	G. Nguyen Lee

PTO
JC474 U.S.
09/620918
07/21/00

U.S. PATENT DOCUMENTS

EXAMINER INITIALS	DOCUMENT NUMBER	ISSUE DATE	PATENTEE	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE

FOREIGN PATENT DOCUMENTS

EXAMINER INITIALS	DOCUMENT NUMBER	PUBLICATION DATE	COUNTRY OR PATENT OFFICE	CLASS	SUBCLASS	TRANSLATION	
						YES	NO

OTHER DOCUMENTS

EXAMINER INITIALS	Include name of the author (in CAPITAL LETTERS), title of the article, title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.
N	Joshi, Robindra B. et al., "WP 4.2: A 100MHz, 5MBaud QAM Decision-Feedback Equalizer for Digital Television Applications" 1994 IEEE International Solid-State Circuits Conference, ISSCC94/Session 4/Video and Communication Signal Processors/Paper WP 4.2 pp. 68-69
N	Martin, James et al., "Local Area Networks Architectures and Implementations" Second Edition, 1994, pp. 167-170, 204
N	Razavi, Behzad, "Monolithic Phase-Locked Loops and Clock Recovery Circuits - Theory and Design" IEEE Press, 1996, Tutorial pp. 2-7
N	Anderson, John B., "Digital Transmission Engineering" IEEE Press, 1999, Synchronization - Chapter 4, pp. 193-196

EXAMINER SIGNATURE		DATE CONSIDERED	2/5/03
EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.			

Patent and Trademark Office; U.S. DEPARTMENT OF COMMERCE